

- Operating temperature range: -40°C to +85°C
- Isolation Voltage: 1500VDC
- Maximum Efficiency: up to 86%
- Continuous Short-Circuit Protection
- Complies with the RoHS Directive



Selection Table

Product Model	Input Voltage (VDC)	Output		Full-Load Efficiency % (Min,Typ)	Maximum Capacitive Load (μF)
	Nominal(Range)	Output Voltage (VDC)	Maximum Current (mA)Max./Min		
LRD10-24S03	24 (9-36)	3.3	2000/0	81/83	2200
LRD10-24S05		5	2000/0	80/87	2200
LRD10-24S09		9	1111/0	82/88	680
LRD10-24S12		12	833/0	83/87	470
LRD10-24S15		15	667/0	83/86	330
LRD10-24S24		24	417/0	83/86	220
LRD10-24D05		±5	±1000/0	78/80	470
LRD10-24D09		±9	±555/0	81/83	220
LRD10-24D12		±12	±417/0	81/83	120
LRD10-24D15		±15	±334/0	81/83	100
LRD10-24D24		±24	±209/0	80/82	68
LRD10-48S03	48 (18-75)	3.3	2000/0	81/83	2200
LRD10-48S05		5	2000/0	80/82	2200
LRD10-48S09		9	1111/0	82/84	680
LRD10-48S12		12	833/0	83/85	470
LRD10-48S15		15	667/0	83/86	330
LRD10-48S24		24	417/0	83/86	220
LRD10-48D05		±5	±1000/0	78/80	470
LRD10-48D09		±9	±556/0	81/83	220
LRD10-48D12		±12	±417/0	81/83	120
LRD10-48D15		±15	±334/0	81/83	100
LRD10-48D24		±24	±209/0	80/82	68

Input Characteristics

Parameter	Condition	Min.	Typ.	Max.	Unit
Input Current (No Load)	24VDC Input		7	25	
	48VDC Input	--	6	25	
Reflected Ripple Curren		--	50	--	
Surge Voltage	24VDC Input	-0.7	--	50	
	48VDC Input	-0.7	--	100	
Startup Voltage	24VDC Input	--	9	--	
	48VDC Input	--	18	--	
Input Filter Type		Capacitive Filter			
Hot Swap		Not Supported			
Remote Control Pin(CTRL)	Module Shutdown	0-1.2V Shutdown			
	Module ON	Floating or 3.5-12V Startup			

Output Characteristics

Parameter	Condition	Min.	Typ.	Max.	Unit	
Output Voltage Accuracy	5% to 100% Load	--	±1.0	±3.0	%	
Line Regulation	Full Load, Output Voltage from Low to High Limit	--	±0.2	±0.5		
Load Regulation	5% to 100% Load	--	±0.5	±1.5		
Transient Recovery Time	25% Load Step Change (Nominal Input Voltage)	--	0.3	0.5	ms	
Transient Response Deviation		3.3\5VDC Input	--	±5	±8	%
		Other Input	--	±3	±5	
Temperature Coefficient	Full Load	--	--	±0.03	%/°C	
Ripple and Noise	20MHz bandwidth, 5% - 100% load	--	50	100	mVp-p	
Output Overvoltage Protection		110	--	160	%Vo	
Output overcurrent protection		110	140	190	%Io	
Short Circuit Protection		Sustainable, Self-healing				

General characteristics

Parameter	Condition	Min.	Typ.	Max.	Unit
Isolation Voltage	Input-Output, Test Time 1 Minute, Leakage Current < 1mA	1500	--	--	VDC
Insulation Resistance	Input-Output, Insulation Voltage 500VDC	1000	--	--	MΩ
Isolation Capacitance	Input-Output, 100KHz/0.1V	--	1000	--	pF
Operating Temperature		-40	--	+85	°C
Storage Temperature		-55	--	+125	
Storage Humidity	No Condensation	5	--	95	%RH

DC/DC Power Module-10W Wide Voltage



Lead Soldering Temperature	1.5mm from Housing, 10 Seconds	--	--	+300	°C
Switching Frequency	Full Load, Nominal Input Voltage	--	270	--	KHz
MTBF	MIL-HDBK-217F@25°C	1000	--	--	K Hours

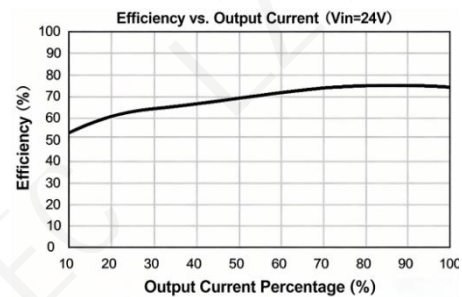
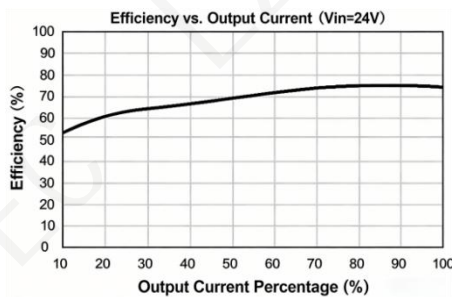
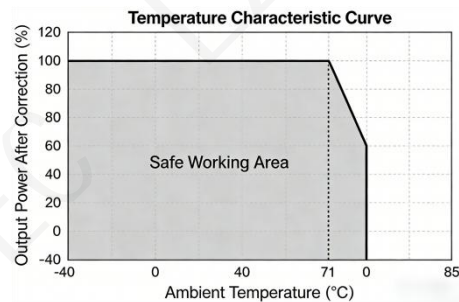
Physical Characteristics

Housing Material	Black Flame-Retardant Heat-Resistant Plastic (UL 94V-0 rated)
Package Dimensions	22.00*9.50*12.00 mm
Weight	4.8g
Cooling Method	Natural Convection

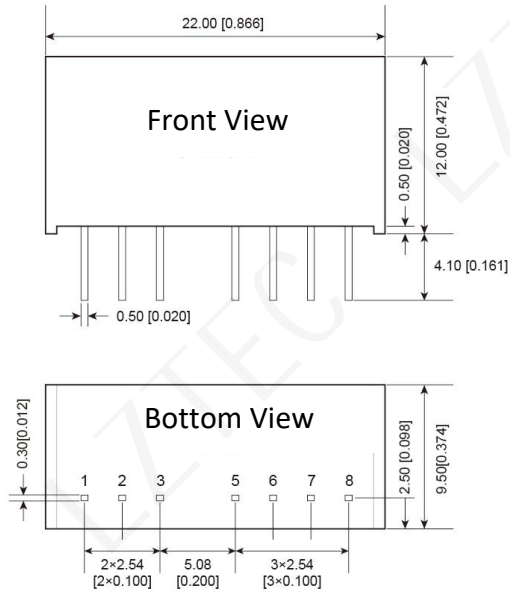
EMC Characteristic

EMI	Conducted Emission	CISPR32/EN55032 CLASS B (Recommended Circuit see Figure 3)
	Radiated Emission	CISPR32/EN55032 CLASS B (Recommended Circuit see Figure 3)
EMS	Electrostatic Discharge	IEC/EN61000-4-2 Contact $\pm 4\text{KV}$ Perf.Criteria B
	Radiated Immunity	IEC/EN61000-4-3 10V/m Perf.Criteria A
	Electrical Fast Transient/Burst	IEC/EN61000-4-4 $\pm 2\text{KV}$ (Recommended Circuit see Figure 3)
	Surge Immunity	IEC/EN61000-4-5 line to line $\pm 2\text{KV}$ (Recommended Circuit see Figure 3)
	Conducted Disturbance Immunity	IEC/EN61000-4-6 3 Vr.m.s Perf.Criteria A

Product Feature Curve Diagram



External dimensions/printing layout recommendations



PIN	Single channel	Double channel
1	GND	GND
2	Vin	Vin
3	CTRL	CTRL
5	NC	NC
6	+Vo	+Vo
7	-Vo	COM
8	NC	-Vo

NC: No Connection - Do not connect to any external components or circuits to avoid device damage.

Size unit: mm[inch]

End diameter tolerance: $\pm 0.10[\pm 0.004]$

Unmarked tolerances: $\pm 0.50[\pm 0.020]$

1. Application Circuit

All DC/DC converters in this series are tested in accordance with the recommended test circuit before delivery. If it is required to further reduce the input and output ripple, the external input and output capacitors C_{in} and C_{out} can be increased in capacitance value or replaced with capacitors with low equivalent series resistance (ESR). However, the capacitance value shall not exceed the maximum capacitive load specified for the product.

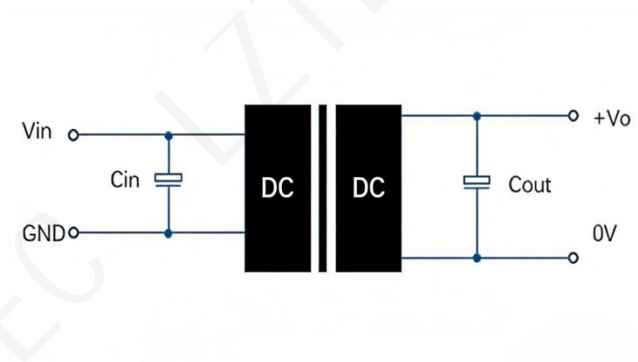


Figure 2

$C_{in}(\mu F)$	$C_{out}(\mu F)$
100	10

2. EMC Solution - Recommended Circuit

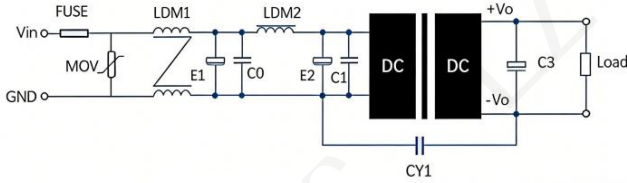


Figure 3

MOV	14D560K
C0、C1	1uF
E1、E2	100μF
LDM1	10mH
LDM2	10μH
CY1	1nF/2KV

NOTE

1. If the product operates below the minimum required load, it cannot be guaranteed that all performance metrics specified in this manual will be met.
2. The maximum capacitive load is tested under the condition of input voltage range and full load.
3. Unless otherwise specified, all measurements in this manual are taken at $T_a=25^{\circ}\text{C}$, temperature $<75\%$ RH, nominal input voltage, and rated output load.
4. All test methods in this manual are based on our company's enterprise standards.
5. We offer customized product solutions. For specific requirements, please contact our technical team directly.

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